



# A Webinar on Empowering India's Space Sector through Semiconductor Excellence



*Organized by*

**Indian National Space Promotion and Authorisation Centre (IN-SPACE)  
Dept. of Space**

**In association with**

**Space Application Centre(SAC)  
Indian Space Research Organisation (ISRO)  
Dept. of Space**

**And**

**Semi-Conductor Laboratory (SCL)  
Ministry of Electronics and Information Technology (MeitY)**

**Date of Webinar: August 08, 2025**



## Introduction

India's space sector is witnessing unprecedented growth, driven by technological innovation, private sector participation, and ambitious national space missions. At the heart of this transformation lies the critical role of semiconductors.

Semiconductors form the backbone of modern space technology, powering satellite communication, navigation, remote sensing, and launch vehicle systems. With India's space industry projected to grow to USD 44 billion by 2033, and with an increase in the demand for radiation-hardened chips, developing a robust semiconductor design and manufacturing ecosystem is crucial for achieving technological self-reliance and global competitiveness.

Considering these aspects, this webinar is planned, aiming to bring together eminent scientists, industry experts, policymakers, and semiconductor innovators to discuss:

- The critical role of semiconductor excellence in satellite design, payload systems, and ground segment infrastructure.
- Current status, gaps, and challenges in India's semiconductor ecosystem for space applications.
- Opportunities for industry collaboration in developing indigenous semiconductor technologies.
- Explore policy initiatives, R&D opportunities, and industry collaborations for creating a self-reliant, space-grade semiconductor ecosystem.
- Pathways for start-ups and private players to leverage semiconductor advancements for next-generation space applications.

## Agenda

Time (Hrs)	Agenda	By
09.30-09.35	Welcome	Dy. Director, PD, IN-SPACe
09.35-09.45	Opening Remarks and brief on webinar	Director, PD, IN-SPACe
09.45-09.55	Special Address: India Semiconductor Mission (TBC)	Addl. Secretary, MeITY
09.55-10.15	Keynote Address: The Critical Role of Semiconductors in Space Technology	Director, SAC
10.15-10.25	SCL Overview & Vision	DG, SCL
10.25-11.10	VLSI Design & IPs	SCL
	VLSI Process Technology & Fabrication	
	Assembly, Testing, Marking & Packaging (ATMP)	
	Reliability & Quality Assurance for Space grade products	
11.10-11.30	Design for Space- Opportunities and challenges	SAC
11.30 -11:40	Ecosystem & Supply Chain Development/ Requirements for Next-Gen Space Systems	IN-SPACe
11:40-11:50	Policy & Incentives for Semiconductor Manufacturing	MeitY
11:50-12:15	Presentation by space start-up's/incubators on their requirement/product development	Start-ups /incubator
12:15-12:35	Presentation by Semiconductor Industries on their existing and upcoming capabilities	Industries
12:35-12:45	Open Floor Discussion a. Identifying gaps and roadmap	All Participants
12:45-12:55	Closing Remarks	IN-SPACe, SCL & SAC
12:55-13:00	Vote of Thanks	IN-SPACe

## Target Audience

- Space industry professionals, policymakers, semiconductor designers and manufacturers, researchers.
- representatives from ISRO, IN-SPACe, SCL, academic & R&D institutions, and investors.

## Expected Outcomes

- Identification of key enablers and policy interventions for semiconductor development in the space sector.
- Enhanced industry-academia-government collaboration for design and manufacturing.
- Roadmap for indigenous space-grade semiconductor solutions.

## Registration

The participation is by invitation and registration by the concerned NGEs & public organisations. There is no registration fee for participation.

Reg Link – [https://www.inspace.gov.in/inspace?id=inspace\\_webinar](https://www.inspace.gov.in/inspace?id=inspace_webinar)

Last Date for Registration – **August 06, 2025**

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